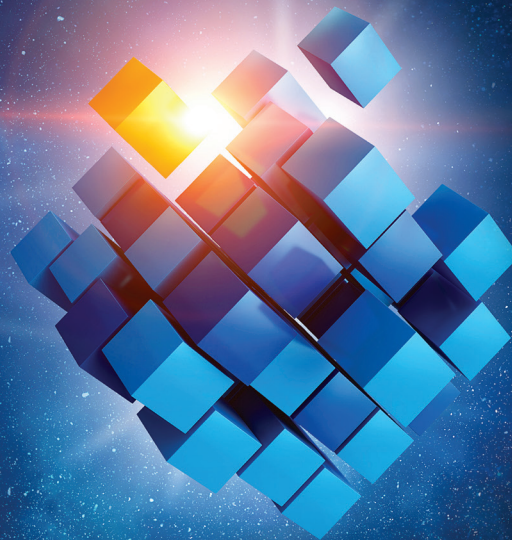


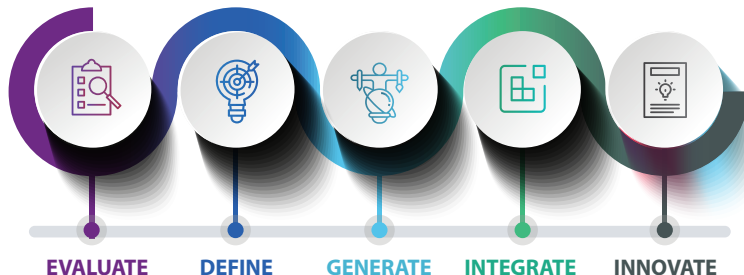


## eFPGA IP Generator

Enables rapid prototyping of customizable FPGA architectures



Australis is QuickLogic's eFPGA IP Generator, blending our three decades of deep domain expertise in designing and shipping programmable logic architectures that are silicon-efficient, reliable, high quality, and manufacturable at scale with the silicon design automation capabilities of the OpenFPGA project.



### Features

- eFPGA IP can be generated / customized to any foundry / process technology within days for a process we already support and within a quarter for new process technologies
- eFPGA optimizations for performance, power or area are available
- Multiple eFPGA architectures from LUT4 to LUT6, variable routing density, and more
- Variety of eFPGA array sizes with option to include Block RAM and/or DSPs blocks

### Benefits

- Fast-time to market – Customized eFPGA IP cores that meet your development window
- Flexibility to meet your SoC design requirements – Optimized for your Power, Performance and Area requirements
- Cost effective – our automated approach means cores are customized to your requirements with substantially less cost and time, meeting your cost target

Learn more on the web



Corporate Headquarters:  
2220 Lundy Drive, San Jose, CA 95131 USA  
1-408-990-4000 | [info@quicklogic.com](mailto:info@quicklogic.com):

Sales Offices: <https://www.quicklogic.com/company/sales-locations/>  
North America: [america-sales@quicklogic.com](mailto:america-sales@quicklogic.com) | China: [asia-sales@quicklogic.com](mailto:asia-sales@quicklogic.com) | Japan: [japan-sales@quicklogic.com](mailto:japan-sales@quicklogic.com)  
Korea: [korea-sales@quicklogic.com](mailto:korea-sales@quicklogic.com) | Taiwan: [asia-sales@quicklogic.com](mailto:asia-sales@quicklogic.com) | United Kingdom: [europa-sales@quicklogic.com](mailto:europa-sales@quicklogic.com)

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