Programmable Logic for Mission Critical Applications

QuickLogic delivers eFPGA IP Cores and FPGA Devices tailored for the needs of your program. Our design methodology is based on a highly automated flow that uses standard cell ASIC libraries. Our workflow enables rapid architecture explorations to optimize the FPGA technology for power, performance, area, and environmental constraints.

Experts in Programmable Logic – 30 years making FPGA devices

Experience in defense
QuickLogic has a long history supplying programmable logic to all the top defense contractors.

Routability
FPGA designs need to be placed and routed in the programmable logic device. Finding the optimal amount of routing channels proportionate to LUTs/BRAMs/DSPs is critical.

Proven design flow
QuickLogic’s Australis eFPGA IP Generator uses proven standard cell libraries for the target process, including specialized libraries such as RHBD, automotive, etc.

eFPGA/FPGA User Tools
The Aurora Development Tool Suite is based on a fully open-source implementation for scalability, longevity, and full code transparency.

Architecture definition
Selecting an architecture that meets the mission requirements for performance, power and area.

eFPGA IP or FPGA devices?
QuickLogic can do both! You can select eFPGA IP if you are building your own SoC, or let us handle the design, verification, production and the supply chain for finished FPGA devices. The choice is yours!

Customized security
QuickLogic works with partners in the security for bitstream encryption, system root of trust, and many customized security implementations to meet the mission requirements.

Made in the USA
QuickLogic is a publicly traded company (Nasdaq: QUIK) with all research and development taking place in the USA, and the company is fully ITAR compliant.

DARPA Toolbox Initiative
QuickLogic is a proud participant in the U.S. defense Advanced Research Projects Agency (DARPA) Toolbox Initiative as an authorized supplier of embedded FPGA IP and open-source FPGA tools.